

UPDATED ON : 11 - 09 - 2018



1. Name : Pushpalatha O
2. Designation : Assistant Professor
3. Department : Electronics & Communication Engineering
4. Academic Background :

	College/University	Department/Specialization
Ph.D.	--	--
M.Tech.	BIET / VTU	CSE
BE	STJIT / VTU	ECE

5. Contact number :9980708881
6. Email id: (jitd.in and any) :pushpalathao@jitd.in / pushpalatha.sonu@gmail.com
7. Date of Joining Jitd :22 – 07 - 2013
8. Experience (in years) :

Research	Industry	Academic
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9. Publication Statistics :

	International	National
Conference	---	---
Journal	02	---

10. Areas of Interest : Image Processing

11. List of Conferences / Symposiums / Summer Schools / Faculty Development

Programs and Workshops Attended or Volunteered.

- i) Attended Two week ISTE STTP on CMOS, Mixed Signal and Radio Frequency VLSI Design from 26th Dec 2016 to 4th Feb 2017 organized by UBDTCE, Davangere.
- ii) Attended Deep Dive Faculty Enablement Program on “Foundation Program 3.1” from 28th Jan 2013 to 1st Feb 2013 organized by BIET, Davangere
- iii) Attended ‘FEEL Teacher’ Learning And Development Intervention conducted from 19th to 21st April 2013 organized by BIET, Davangere
- iv) Attended Workshop on “Embedded System Design” on 27th April 2007 organized by IPC Bangalore and BIET, Davangere
- v) Attended Workshop on “Tanner Tools Pro (A complete VLSI Design System)” on 16th November 2006 organized by BIET, Davangere; Sovereign Technologies, B’lore.

12. List of significant publications (in IEEE citation format)

a) International Journals

- i) Pushpalatha O “A brief study on Stenographic techniques” IJAEMS, Vol. 2, Issue 4, pp 180 – 183 April 2016 (Thomson Reuters)
- ii) Pushpalatha O “Arduino Based Fire Accident Detection in Train using GSM and GPS Technology” IJMTES, Vol. 4, Issue 5, 2017.