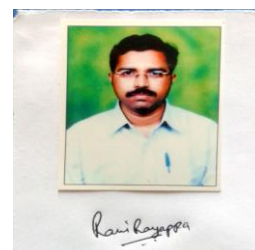


## PROFILE OF FACULTY



1. NAME: Mr. Ravi rayappa

2. DATE OF BIRTH: 15-06-1978

3. ADDRESS, ,

4. OFFICE/COLLEGE: Mr. Ravi rayappa

Asst .Prof, Dept of ECE,

Jain Institute of Technology, Near Veereshwara Punyashrama

Bada cross,

Davangere

### RESIDENCE:

5. : Mr. Ravi rayappa

Asst. Prof, Dept of ECE,

Jain Institute of Technology, near veereshwara punyashrama

Bada cross,

Davangere

6. CONTACT NO: 7795446021

LAND LINE: -----

EXT: -----

7. FAX:-----

8. EMAIL ID: ravi123447@rediffmail.com

9. DESIGNATION: Asst Professor

10. DEPARTMENT: Electronics and Communication Engineering

11. DATE OF JOINING: 16<sup>th</sup> , August 2011

12. EXPERIENCE (YEARS)

TEACHING: 14 years

INDUSTRY: ----

RESEARCH: ----

**13. QUALIFICATIONS:**

	University	Specialization	Year of passing	Class
M.Tech	VTU ,Belgaum	VLSI design and embedded system	2008	First Class
B.E.	Karnataka University ,Dharwad	Electronics and Communication Engineering	2001	First Class
DIPLOMA	Board of technical Examinations ,DTE, Bangalore	Electronics and Communication Engineering	1997	First Class

**14. EXPERIENCE: (reverse chronological order)**

Positions	Institution/Industry	Period	Nature of Job	No of Years
Assistant Professor	Jain Institute of Technology, Davangere	August 16 <sup>th</sup> 2011 to till date	Teaching	6 years
Lecturer	GMIT, Davangere	2 <sup>nd</sup> February to 2009 28 <sup>th</sup> July 2011	Teaching	1.5 years
Lecturer	East West Institute of Technology, Bangalore	13 <sup>th</sup> august 2007 to 28 <sup>th</sup> January 2009	Teaching	1.6 years
Lecturer	GMIT, Davangere	1 <sup>st</sup> September 2004 to 10 <sup>th</sup> June 2006	Teaching	1.10 years
Lecturer	Sree Bahubali Polytechnic, Shravanabelogola	2001-2003	Teaching	1.8 years

**15. FACULTY DEVELOPMENT PROGRAMMES/TRAINING/WORKSHOP/SUMMER/WINTER SCHOOLS ATTENDED (reverse chronological order)**

SI No	Name of training/workshop	Year/Duration	Organizers
1	Digital VLSI Testing	24 <sup>th</sup> July -28 <sup>th</sup> July 2018	A.P.S College of Engineering Bangalore
2	Hands on session in LATEX and research methodology	16 <sup>th</sup> to 21 <sup>st</sup> Jan 2017	RYMCE Bellari Dept of ECE
3	Plagiarism issues	2016 ,20 <sup>th</sup> Feb / 1 day	G.M.I.T ,Davangere +ISTE
4	FEEL TEACHER	2016,3 Days	J.I.T Davangere.
5	Introduction To Robotics	2011, APRIL 26,27	IIT Bombay, at shimogga
6	Digital Designing For FPGA'S using VHDL	2009, 5 <sup>th</sup> TO 9 <sup>th</sup> April	RYMEC ,and sandeepani school of VLSI design and advanced system design and CG-COREL/Program Solution, Bangalore
7	Analog and mixed mode VLSI Design	2009, 20 <sup>th</sup> ,21 <sup>th</sup> March	Dept of ECE ,VDRIT ,HALIYAL
8	Analog and mixed mode Design USING Cadence tool	2009, Feb 2 <sup>nd</sup> to 4 <sup>th</sup>	BNMIT and Cadence design systems(l) pvt ltd.,
9	Latest trends in embedded systems	2008 ,27 <sup>th</sup> Sept	New horizon college of engineering ,Bangalore and sponsored by ISM Bangalore and ISTE, Bangalore chapter
10	Synthesis and Simulation of HDL's(Theory and Laboratory)	2008 ,28 <sup>th</sup> feb to 1 <sup>st</sup> March (04 days)	EWIT, Bangalore, SITECH Electronics ,Bangalore and IETE Bangalore chapter.
11	Micro controllers 8051	2008, from 30 <sup>th</sup> Jan to 2 <sup>nd</sup> Feb,2008	M.S Ramaiah Institute of Technology under TEQIP.
12	Design of VLSI and Micro Controller circuits	2005 / 22 <sup>nd</sup> to 26 <sup>th</sup> August(05 Days)	ISTE and VTU, CMR Institute of Technology, dept of ECE

**16. FACULTY DEVELOPMENT PROGRAMMES/TRAINING/WORKSHOPS/SUMMER/WINTER SCHOOLS ORGANISED** (reverse chronological order)

SI No.	Title/topic	Year & Duration	Funded by	Organizers
1	Plagiarism and essential tools for research write-up's	2015 / 1 Day	Management of J.I.T	Department of ECE, J.I.T, Davangere.
2	National level technical symposium for paper presentation on recent trends in advanced technology Wiztec -2009	2009	Dept of ECE	Dept of ECE

**17. FACULTY AS RESOURCE PERSONS IN FACULTY DEVELOPMENT PROGRAMME/TRAINING/WORKSHOP/SUMMER/WINTER SCHOOLS** (reverse chronological order)

SI No	Name of FDP/training/workshop	Year/Duration	Funded by	organizers
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**18. CONFERENCE /SEMINARS/PROFESSIONAL SOCIETY ACTIVITIES/EVENTS ATTENDED** (reverse chronological order)

SI No.	Title/topic	Year & Duration	Funded by	organizers

**19. CONFERENCES/SEMINARS/PROFESSIONAL SOCIETY ACTIVITIES/ EVENTS ORGANIZED** (reverse chronological order)

SI No.	Title/topic	Year & Duration	Funded by	Organizers
1	NCRTCC-2016	2016-1 DAY----	MGMT+VTU	J.I.T. Davangere.
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**21. PAPERS PUBLISHED:**

SI No	Type of publication	No. of publications
1	International Journal	02
2	National Journal	--
3	International Conferences	01
4	National Conferences	06

**22. DETAILS OF PAPER PUBLISHED (along with DOI and publication/citation details) (reverse chronological order)**

SI No	Title of paper/Report	Author (s)	Name & volume of Journal & Year
1	Assistive device for speech impaired	Mr. Ravi rayappa	Emerging trends in Engineering Research and Management
2	Data acquisition using Zigbee Technology	Mr. Ravi rayappa	NCRTCC- 2016 , J.I.T. Davangere
3	Clock distribution schemes and power distribution at different levels in VLSI design	Mr. Ravi rayappa	NCRTCC- 2016 , J.I.T. Davangere
4	New type of swarm intelligence formulation in mobile vehicular agents	Mr. Ravi Rayappa ,Dr T.C Manjunath	National Symposium on Instrumentation (NSI-38) , from 24 <sup>TH</sup> to 26 <sup>TH</sup> OCTOBER 2013
5	Inverse Kinematics equation formulation for 3-axis robots	Mr. Ravi Rayappa ,Dr T.C Manjunath	National Symposium on Instrumentation (NSI-38) , from 24 <sup>TH</sup> to 26 <sup>TH</sup> OCTOBER 2013
6	Pre & post processing concept development in recognition of characters alphabets in IP	Mr. Ravi Rayappa ,Dr T.C Manjunath	National Symposium on Instrumentation (NSI-38) , from 24 <sup>TH</sup> to 26 <sup>TH</sup> OCTOBER 2013
7	Design of online interactive data acquisition and control system for embedded real time application	Mr. Ravi Rayappa ,Kavitha K.J	International Journal of Applied Science and Technology .ISSN NO.2250-2718(PRINT) EISSN NO 2250-2726(ONLINE) ,VOLUME 3 ISSUE 1 JAN-FEB 2013
8	Online Examination System and purpose-A Review	Mr. Ravi rayappa,	12 <sup>th</sup> ISTE State Level Annual Convention and 2 Day National Seminar ,On 20 <sup>th</sup> and 21 <sup>th</sup>

			November 2009
9	Planar articulated robotic arm design and development	Mr .Ravi rayappa, Dr T.C Manjunath	EITES, National Conference Aug-24,25-2012 at Bhemanna Khandre Institute of technology Bidar- Bhalki
10	Platform support for M-Learning in Communications	Mr.Ravi rayappa	ICCTEM-2012, July 12-14 , VVCE,Mysore.

**23. BOOKS / CHAPTERS PUBLISHED & E-LEARNING MATERIALS DEVELOPED**

**24. FACULTY INTELLECTUAL PROPERTY RIGHTS (Give details)**

**25. FUNDED R & D PROJECTS & CONSULTANCY WORK (Give details)**

SL NO	TITLE OF THE PROJECT	FUNDING AGENCY	AMOUNT
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**26. FACULTY INTERACTION WITH OUTSIDE WORLD (Give details)**

**27. AWARD / RECOGNITION**

**28. NUMBER OF PROJECTS GUIDED**

UG: 06

PG: ---

PhD: ---

**29. SUBJECTS TAUGHT**

- 1) Basic Electronics
- 2) Electronic Instrumentation
- 3) Fundamentals of HDL
- 4) Analog and mixed mode VLSI design
- 5) Micro processor 8085
- 6) Solid state device and Technology
- 7) Real time systems
- 8) Micro processor 8086
- 9) Logic design
- 10) Micro controller
- 11) Constitution of India and professional ethics
- 12) Computer networks
- 13) CMOS VLSI design

- 14) Embedded systems.
- 15) Real Time systems
- 16) Management and Entrepreneurship
- 17) Constitution of India and professional ethics.
- 18) Verilog HDL

### **30. MEMBERSHIP OF PROFESSIONAL BODIES**

Member of International Association of Engineers [IAENG]  
Member Number. **218968**

### **31. BRIEF ON ACTIVITIES FOR INSTITUTIONAL DEVELOPMENT**

- a) Established all the laboratories of ECE Dept, in coordination with all the faculties.
- b) Sports coordinator for the college Event [SHRUNGA], for a period of 2 years (2011 to 2012).
- c) Counseling coordinator for the college for a period of 3 years (2011 to 2013).
- d) Examination coordinator for VTU exams in the college for 1 year.
- e) Stock registers Coordinator for the labs of Electronics and Communication Engineering Department.
- f) Alumni Coordinator for Electronics and Communication Department.
- g) Department In-charge HOD for a period of 3 years.
- h) Member of Admission Committee
- i) Member of Publicity Committee
- j) Member of Admission Committee
- k) Member of Anti-ragging Committee
- l) I.S.T.E coordinator
- m) NBA committee member in the department of Electronics and Communication Engineering

### **32. OTHER ASSIGNMENTS**