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JAIN INSTITUTE OF TECHNOLOGY

(A UNIT OF JAIN GROUP OF INSTITUTIONS, BANGALORE)

(Approved by AICTE, New Delhi, Recognized by Government of
Karnataka, Affiliated to VTU, Belagavi)

#323, Near Veereshwara Punyashrama, Davanagere - 5770003



FIVE DAY FACULTY DEVELOPMENT PROGRAM

ON

"ABILITY ENHANCEMENT COURSES"

13TH - 17TH DECEMBER, 2022



IN COLLABORATION WITH

**VISVESWARAYA TECHNOLOGICAL UNIVERSITY,
BELAGAVI**

&

**DEPT. OF ELECTRICAL & ELECTRONICS ENGINEERING
SJBIT-BANGALORE**



ORGANIZED BY:

Department of Electrical & Electronics Engg.

JAIN INSTITUTE OF TECHNOLOGY,

DAVANAGERE.

ABOUT JIT

JIT is one of the best developing engineering colleges of Davanagere and is located adjacent to Pune Bangalore National Highway. It is established in the year 2011. JIT is 7 Kms from Davanagere Railway Station. It has lush green campus spread across 16 Acres land, attached with Lake. The College has excellent infrastructural facilities that include spacious class rooms attached with tutorial rooms. Well-equipped Laboratories, Workshops, Drawing Halls, Seminar Halls, Digital Library, Staff Rooms, Computer Center, Browsing Centre with high speed internet connectivity, Girls & Boys Common Room, Canteen, Transportation and reprographic facility to the students etc. Well experienced & dedicated faculty members, a strong students mentorship program.

INSTITUTE VISION

Technical manpower development to build professionally excellent, globally competitive, socially responsible engineers and entrepreneurs with human values.

INSTITUTE MISSION

- M-1:** To provide quality education through innovation in teaching to create technologically competent engineers.
- M-2:** To Achieve excellence in research and development to advance science and technology to the ever changing needs of society.
- M-3:** To create outstanding professionals capable of working in multi-cultural environment.
- M-4:** To produce quality engineers with high ethical standards and professionalism.

ABOUT DEPARTMENT

UG course Electrical & Electronics engineering department is established in the year 2011 in JIT with the intake of 60. The department has good infrastructure, qualified teaching and non-teaching staff and well equipped laboratories. The department has adopted Outcome Based Education to improve the quality of teaching and learning process. Faculty members have published good number of research publications in peer-reviewed journals and have many patents. The department has filled funding's to various agencies like VGST, KSCST etc. The department has MoU with more than seven companies and have been conducting many workshops in collaboration with these companies under the Forum POTENZA.

DEPARTMENT VISION

To create technically superior, highly competent, ethically strong and socially responsible Electrical and Electronics Engineers with innovative and entrepreneurship qualities.

DEPARTMENT MISSION

- M-1:** To impart excellent Electrical and Electronics Engineering Education by providing Innovative teaching, career focused education and experiential learning.
- M-2:** To create an environment for innovative thinking and contribute to the advancement of science and technology to meet the ever changing needs of society.
- M-3:** To provide student centric platform to produce socially responsible, competent engineers and entrepreneurs with ethical standards.

ABOUT FDP

Practical exposure is much needed to relate to theoretical learning. Scilab is a free and open-source, cross platform numerical computational package and a high level numerically oriented programming language. Scilab also includes a free package called Xcos for modelling & simulation of explicit and implicit dynamical systems, including both continuous and discrete subsystem. Xcos is the open source equivalent to Simulink from Math Works. PSPICE simulation technology combines industry- leading, native analog and mixed-signal engines to deliver a complete circuit simulation and verification solution for Electrical-applications. In this context Department of EEE, JIT has always tried to give best efforts to make right from organizing webinar series, training programs, workshops and FDP's. In this perspective, we have come up with a Five-day FDP, Collating experts in the field of Scilab and Pspice usage for Electrical engineers. We have organized a series of sessions concentrating on futuristic trends in the field of Scilab-usage for different electrical laboratory applications.

Contents Covered:

- 21EEL381: Scilab for Transformers and Generators
- 21EEL382: Circuit lab using Pspice.
- 21EEL384: Scilab for Mathematics.
- 21EEL482: Scilab for Electric Motors.
- 21EEL582: Scilab for Power Electronics.

Five day Faculty Development Program

On

“ABILITY ENHANCEMENT COURSES”

13th – 17th December, 2022

In Collaboration with

Visveswaraya Technological University,
Belagavi.

&

Dept. of Electrical & Electronics Engineering
SJBIT-Bangalore.

ORGANIZED BY:

Department of Electrical & Electronics Engg.
Jain Institute of Technology, Davanagere.

REGISTRATION

(Open to Faculties of all Branches-Use link or
QR code for registration)

Registration Fees: Rs 500/-

Last date for registration: 9th Dec 2022

REGISTRATION LINK:

<https://forms.gle/FJFaaRdY3AqAM8138>



ACCOUNT DETAILS:

Account Name: Principal, JIT
Bank: Federal Bank, PJ Extn Branch, Davanagere
IFSC Code: FDRL0002297
Account No: 22970100029915

CERTIFICATES:

Certificates will be provided to the
participants who will attend all the sessions
VENUE: JIT AUDITORIUM